Application Note

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MP1800A 28G/32G Generate Stairstep Test Waveform for PAM4

MP1800A Series Signal Quality Analyzer This application note describes how to generate TX stair-step test waveform for PAM4 by using the MP1800A Signal Quality Analyzer.

TX stair-step test waveform is recommended in the **PAM4 TX Specification for IEEE 802.3 100Gb/s Backplane and Copper Cable Task Force**, please see Figure 1 below. On the MP1800A, users can get the Tx stairstep waveform by setting PPG Channel Synchronization mode, users' data patterns, and the data output amplitude of one channel is half of the other channel on the PPG.

A Proposed TX Stairstep Test Waveform for PAM4:

- PAM4 test waveform is period 80T with stair steps up, stair steps down, and full swing transitions.
- Each transmitted level is of duration 8 Baud periods.



Figure 1. Tx staristep test waveform is recommended in the PAM4 TX Specification for IEEE 802.3 100Gbps over backplane & copper cable task force

Equipment (minimum):

- MU183020A 28/32Gb/s 2- channel PPG
- MU181000A/B Synthesize
- PAM4 converter MZ1834A or MZ1834B

Connections and configurations:

Please connect External clock input on PPG with the clock output of a synthesizer MU18100A/B (or thru jitter card MU181500B), and install a PAM4 converter MZ1834A/B on the PPG. Set the two channels of PPG to "Channel Synchronization mode", please refer to Figure 2 below, and set the data output amplitude of one channel is half of the other channel on the PPG, for example, Channel 1 (Data1) with 3.5Vpp, and Channel 2 (Data2) with 1.75Vpp, please see Figure 3 and Figure 4 below.

Then configure the data patterns:

- PPG1 80bits 0000 FFFF 00FF 00FF FF00
- PPG2 80bits 00FF 00FF 00FF 00FF

Please refer to Figure 5 and Figure 6.

Turn on the data output ON the PPG of MP1800A.

The MZ1834A/B is a PAM4 converter, that has attenuators and power dividers inside.

As we can see, users can generate the Tx stairstep waveform quickly by using the MP1800A, which can be used to measure the signal levels for PAM4, and test the loss and distortion on a DUT by using an oscilloscope as required in the IEEE standards, please see comment **"TX Specification for PAM4 Levels**" below.

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Figure 2. click "Setting" at Misc2 on the PPG, to set Data1 and Data2 to Channel Synchronization mode

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Figure 3. set amplitude of Data1 output on PPG with 3.5Vpp

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Figure 4. set amplitude of Data2 output on PPG with 1.75Vpp

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Figure 5. set the data pattern on Data1 of PPG

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Figure 6. set the data pattern on Data2 of PPG

TX Specification for PAM4 Levels*:

- The most important specification for PAM4 is that the four levels (w/o TX de-emphasis) are approximately equally spaced, else low frequency Signal to Distortion Ratio (SDR) suffers
 - Define $V_{LOW} = (V_C V_B)/2$
 - Define $V_{HIGH} = (V_D V_A)/6$
 - Define $V_{AVG} = (V_{HIGH} + V_{LOW})/2$
 - Spec |V_{HIGH} V_D/3| < 0.06 V_{AVG}
 - Spec |V_{HIGH} + V_A/3| < 0.06 V_{AVG}
 - Spec $|V_{LOW} V_C| < 0.06 V_{AVG}$
 - Spec $|V_{LOW} + V_B| < 0.06 V_{AVG}$

More specification can be tested with the single proposed waveform and capture.

- Symmetry between 'up' and 'down' steps
 - Rise and fall times
 - Duty cycle
 - Symmetry between steps of magnitude 2 and steps of magnitude 6
 - Rise and fall times the same

*TX specification for PAM4 levels, proposed in IEEE 802.3 100Gbps over backplane and copper cable task force

 V_{AVG} is the amount of noise voltage at the slicer that will cause an error.

<u>/inritsu</u>

United States

Anritsu Company 1155 East Collins Blvd., Suite 100, Richardson, TX 75081, U.S.A. Toll Free: 1-800-267-4878 Phone: +1-972-644-1777 Fax: +1-972-671-1877

Canada

Anritsu Electronics Ltd. 700 Silver Seven Road, Suite 120, Kanata, Ontario K2V 1C3, Canada Phone: +1-613-591-2003 Fax: +1-613-591-1006

Brazil Anritsu Eletrônica Ltda.

Praça Amadeu Amaral, 27 - 1 Andar 01327-010 - Bela Vista - São Paulo - SP - Brazil Phone: +55-11-3283-2511 Fax: +55-11-3288-6940

Mexico

Anritsu Company, S.A. de C.V. Av. Ejército Nacional No. 579 Piso 9, Col. Granada 11520 México, D.F., México Phone: +52-55-1101-2370 Fax: +52-55-5254-3147

• United Kingdom Anritsu EMEA Ltd.

Anritsu EMEA Lto. 200 Capability Green, Luton, Bedfordshire, LU1 3LU, U.K. Phone: +44-1582-433200 Fax: +44-1582-731303

• France

Anritsu S.A. 12 avenue du Québec, Bâtiment Iris 1- Silic 612, 91140 VILLEBON SUR YVETTE, France Phone: +33-1-60-92-15-50 Fax: +33-1-60-46-10-65

• Germany

Anritsu GmbH Nemetschek Haus, Konrad-Zuse-Platz 1 81829 München, Germany Phone: +49-89-442308-0 Fax: +49-89-442308-55

Italy Anritsu S.r.I.

Anritsu S.r.I. Via Elio Vittorini 129, 00144 Roma, Italy Phone: +39-6-509-9711 Fax: +39-6-502-2425

Sweden Anritsu AB

Kistagången 20B, 164 40 KISTA, Sweden Phone: +46-8-534-707-00 Fax: +46-8-534-707-30

• Finland Anritsu AB Teknobulevardi 3-5, FI-01530 VANTAA, Finland Phone: +358-20-741-8100 Fax: +358-20-741-8111

Denmark
Anritsu A/S
Kay Fiskers Plads 9, 2300 Copenhagen S, Denmark
Phone: +45-7211-2200
Fax: +45-7211-2210

• Russia Anritsu EMEA Ltd. Representation Office in Russia Tverskaya str. 16/2, bld. 1, 7th floor. Russia, 125009, Moscow

Phone: +7-495-363-1694 Fax: +7-495-935-8962

• United Arab Emirates Anritsu EMEA Ltd. Dubai Liaison Office

P O Box 500413 - Dubai Internet City Al Thuraya Building, Tower 1, Suit 701, 7th Floor Dubai, United Arab Emirates Phone: +971-4-3670352 Fax: +971-4-3688460

India

Anritsu India Private Limited 2nd & 3rd Floor, #837/1, Binnamangla 1st Stage, Indiranagar, 100ft Road, Bangalore - 560038, India Phone: +91-80-4058-1300 Fax: +91-80-4058-1301

Specifications are subject to change without notice.

• Singapore

Anritsu Pte. Ltd. 11 Chang Charn Road, #04-01, Shriro House Singapore 159640 Phone: +65-6282-2400 Fax: +65-6282-2533

• P.R. China (Shanghai) Anritsu (China) Co., Ltd.

Rom 2701-2705, Tower A, New Caohejing International Business Center No. 391 Gui Ping Road Shanghai, 200233, P.R. China Phone: +86-21-6237-0899 Fax: +86-21-6237-0899

• P.R. China (Hong Kong)

Anritsu Company Ltd. Unit 1006-7, 10/F., Greenfield Tower, Concordia Plaza, No. 1 Science Museum Road, Tsim Sha Tsui East, Kowloon, Hong Kong, P.R. China Phone: +852-2301-4980 Fax: +852-2301-3545

• Japan

Anritsu Corporation 8-5, Tamura-cho, Atsugi-shi, Kanagawa, 243-0016 Japan Phone: +81-46-296-1221 Fax: +81-46-296-1238

Korea
Anritsu Corporation, Ltd.
5FL, 235 Pangyoyeok-ro, Bundang-gu, Seongnam-si,
Gyeonggi-do, 463-400 Korea

Gyeongji-do, 463-400 Korea Phone: +82-31-696-7750 Fax: +82-31-696-7751

• Australia Anritsu Pty. Ltd.

Unit 21/270 Ferntree Gully Road, Notting Hill, Victoria 3168, Australia Phone: +61-3-9558-8177 Fax: +61-3-9558-8255

• Taiwan Anritsu Company Inc. 7F, No. 316, Sec. 1, NeiHu Rd., Taipei 114, Taiwan Phone: +886-2-8751-1816 Fax: +886-2-8751-1817

